

Preliminary DATA SHEET

CAC30203L

28V, DC-3GHz, CW 200Watts GaN Power Amplifier

Product overview

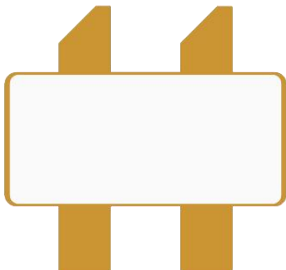
CAC30203L is a 200W(P3dB) internally matched GaN power amplifier. It operates perfectly from DC to 3GHz with 28 volts rail, offers a general purpose, broadband, high power and high efficiency wireless pulse or CW communication application.

Key Features

- DC – 3GHz
- Operating Drain Voltage: +28V
- 18dB small signal Gain @1.3GHz
- 16dB Gain @ 53dBm, 1.3GHz
- 53.5dBm CW Peak Power
- 72.5% Drain Efficiency @53.5dBm,1.3GHz
- NI780 4 Lead flange Ceramic package (MSL3, 260°C per JEDEC J-STD-020)
- ESD Level: HBM TBD ; CDM TBD
- ROHS compatible

Applications

- 3GPP 4G LTE/5G NR massive MIMO basestation
- Driver amplifier for micro-base and macro-base Stations
- Active antenna array
- Pico/Small Cell
- Test Instrumentation
- Industrial, scientific, and medical
- Wideband amplifiers



CAC30203LDF

Figure 1. NI780 4L Package

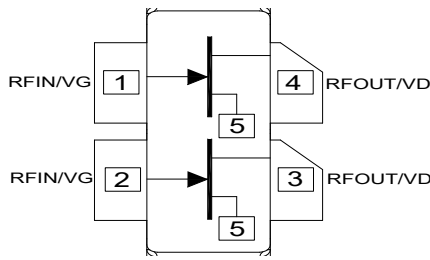


Figure 2. CAC30203L Function Block Diagram

Table 1. CAC30203L Pinout Descriptions

Pin	Name	Description
1,2	RFIN/VG	RF input port with Gate Bias
3,4	RFOUT/VD	RF output port with Drain Supply Voltage
5	GND/Source	Grounding

Ordering Information

Part No.	Description
CAC30203LDF	DC-3GHz 200W GaN PA with Flange, 7' Reel with 500pcs

Table 3. CAC30203L Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Storage Temperature	TSTG	-55 to 125	°C
Operating Temperature	TC	-40 to +105	°C
Operating Junction Temperature	TJ	225	°C
Thermal Resistance	Rθjc		°C/W
Operating Voltage	VDD	0 to 32	V
Drain-Source Voltage	VDSS	180	V
Gate-Source Voltage	VGS	-8 to 0	V
Maximum Forward Gate Current		10	mA
Input Power	PIN	+37	dBm

Table 4. CAC30203L Recommended Operating Conditions

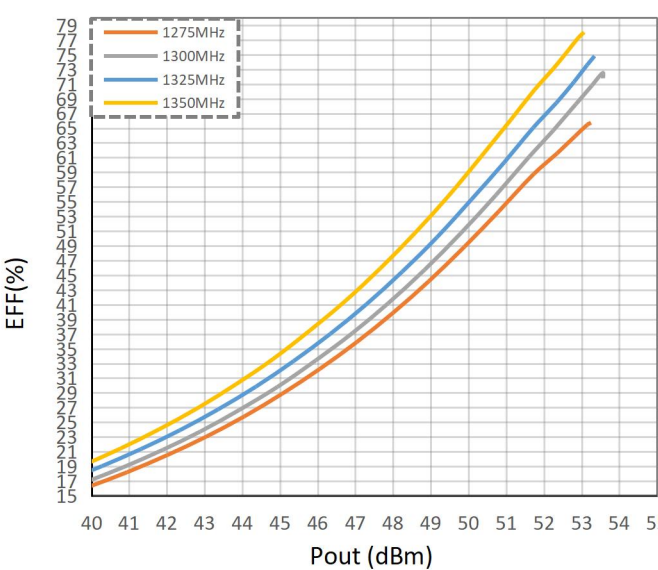
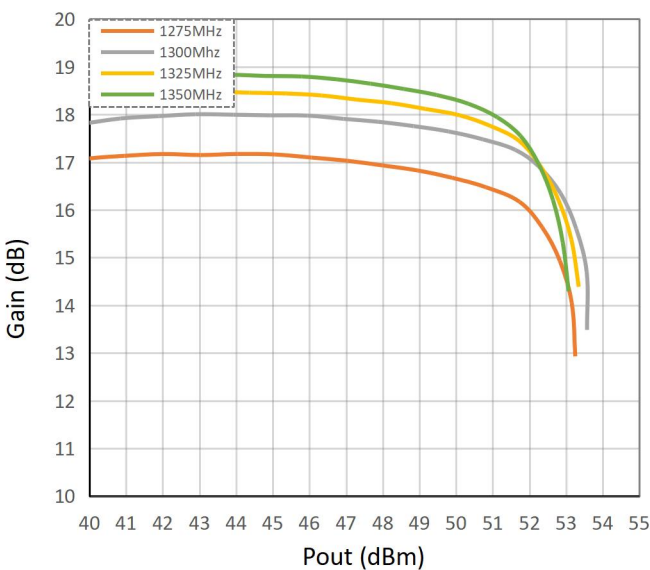
Parameter	Symbol	Min	Typ	Max	Unit
Operating frequency	f	DC		3	GHz
Operating temperature	TC	-40	25	85	°C
Drain Voltage	VDD		28		V
Gate Voltage	VGS		-2.1		V
Quiescent Current	IDQ		550		mA

Table 5. CAC30203L Electrical Specifications

EVb TYPICAL PERFORMANCE: EFFICIENCY TUNED AT P3dB (VD=28V, Idq=550mA, TC=+25 °C, Input /Output Load = 50Ω)

Parameter	Conditions	Min	Typ	Max	Unit
Frequency			1300		MHz
Output P3dB	CW		+53		dBm
Gain @ P3dB	CW		16		dB
Drain efficiency @P3dB	CW		71		%

Table 7. CAC30203L Electrical Specifications



Datasheet: CAC30203L DC to 2GHz 200Watts GaN Power Amplifier

	Layer Stack up	0.9 mm+/-0.1mm
	Silk Top	Default
	Solder Top	
TOP		2.1mil (1.0oz+plating)
CORE (RO4350B)	0.762mm	30mil (RO4350B)
BOTTOM		2.1mil (1.0oz+plating)
	Solder Bot	Default
	Silk Bot	

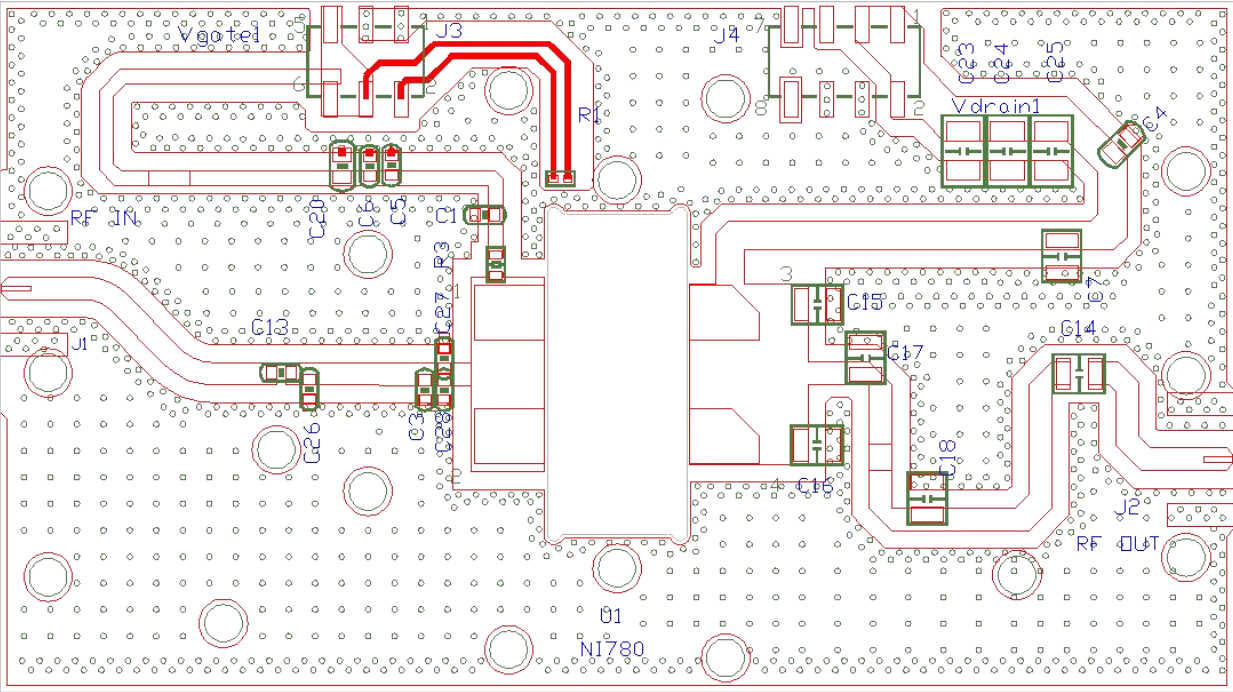


Figure 4. CAC30203L 1250-1350MHz Evaluation Board

Table 6. CAC30203L 1250-1350MHz Evaluation Board Bill of Materials (BOM)

Component	Description	Size
C1, C13	100pF	
C7, C14	51pF, 100V	
C26	2pF	
C3	9.1pF	
C15, C16, C17	10pF 100V	
C27, C28	10pF 50V	
R3	5.6Ohm	
C20	1uF	
C4, C6	100nF 50V	
C5	1nF	
C18	4.7pF, 100V	
C23, C24, C25	10uF 50V	

Rest of the components on the schematic are not used in this part.

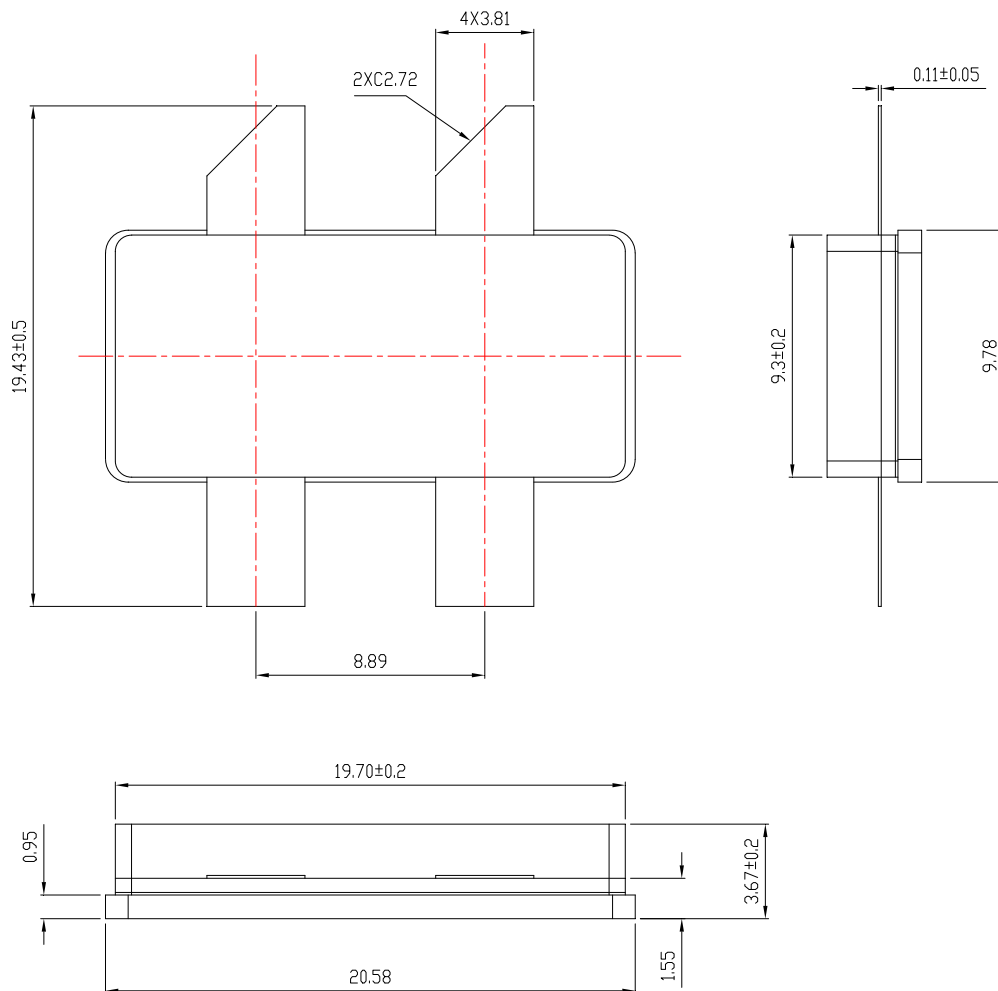
Evaluation board test procedure

Turn-on sequence

- 1 . Connect test equipment to the input and output port of Evaluation board and then connect DC ground.
- 2 . Turn on VG to -7V, turn on VD to 28V then tune VG to 550mA quiescent current in order.
- 3 . Apply RF signal.

Turn-off sequence

- 1 . Turn off RF signal.
- 2 . Turn off VD.
- 3 . Turn off VG.



Notes:

1. ALL DIMENSIONS ARE MM.
2. ALL TOLERANCES $\pm 0.15\text{mm}$ UNLESS SPECIFIED OTHERWISE.

Figure 5. CAC30203L Package Dimensions

CAC30203L REVISION HISTORY

Revision	Date	Description
0.1	2025-4-1	Initial datasheet